

Figures

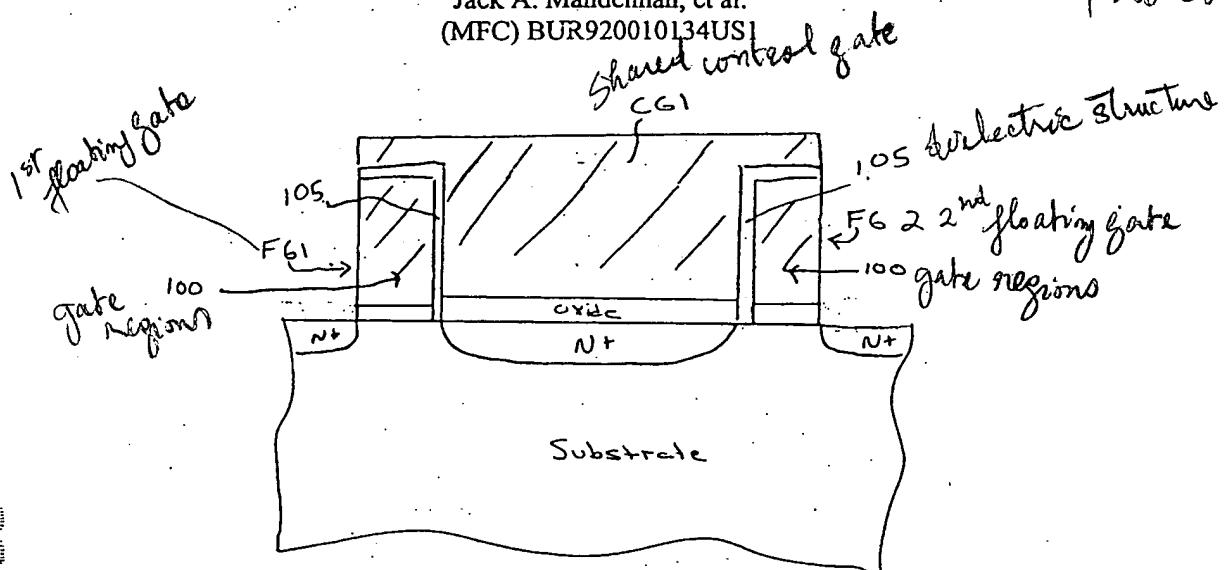


FIG 1 programmable memory cell structure

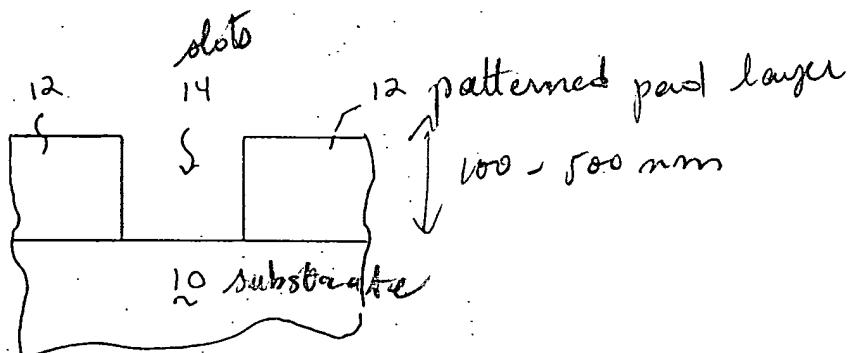


FIG 2

18 floating gate conductor (polySi, W, Ru, ...)

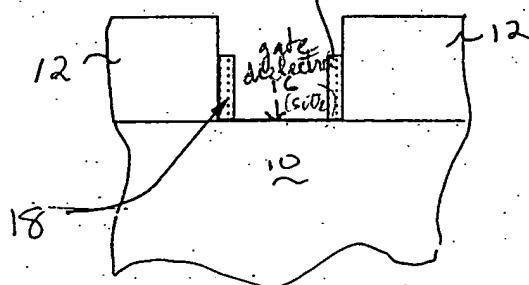


FIG 3

High-density plasma (HDP) oxide

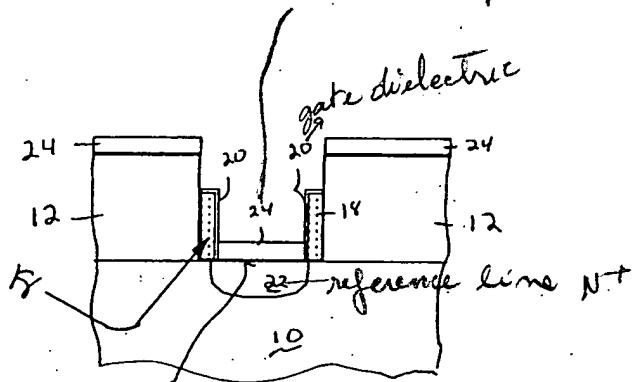


FIG 4

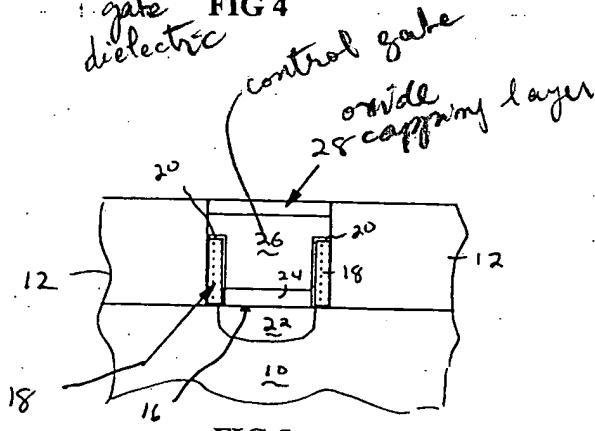


FIG 5

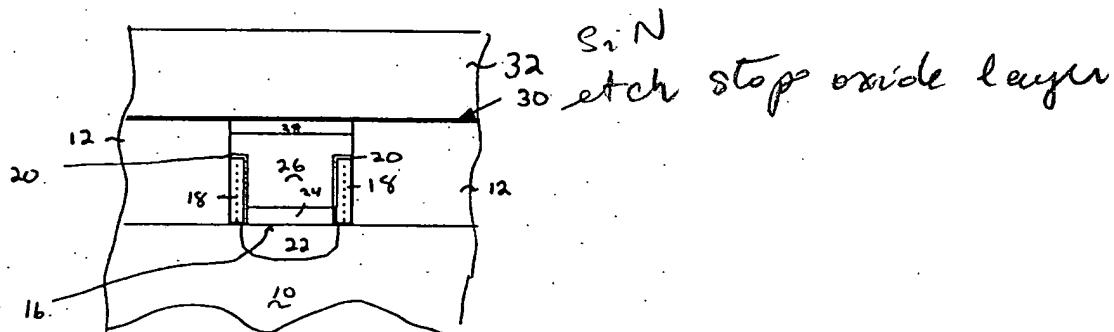


FIG 6

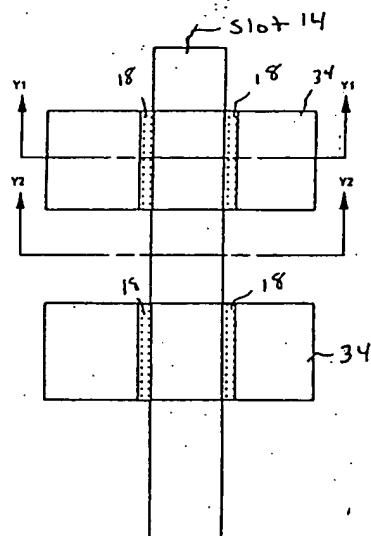


FIG 7A

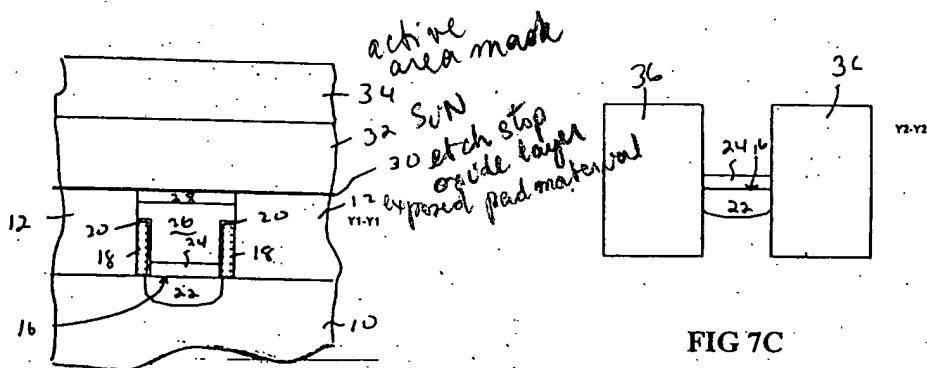


FIG 7B

4/8
BUR920010134US1

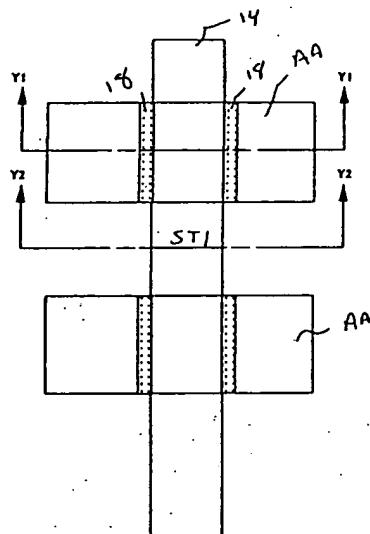


FIG 8A

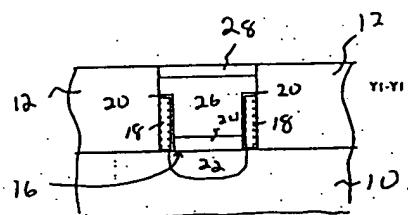


FIG 8B

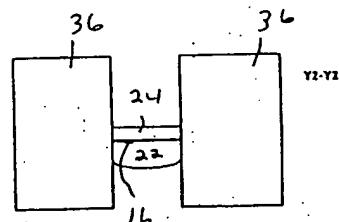


FIG 8C

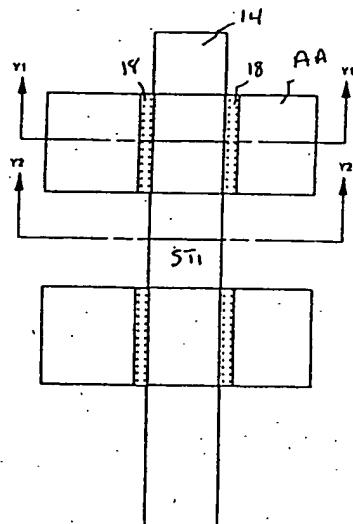


FIG 9A

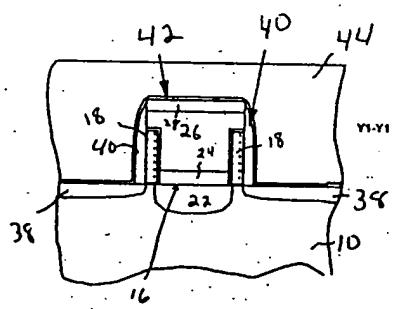


FIG 9B

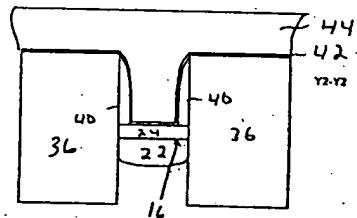


FIG 9C

6/8
BUR920010134US1

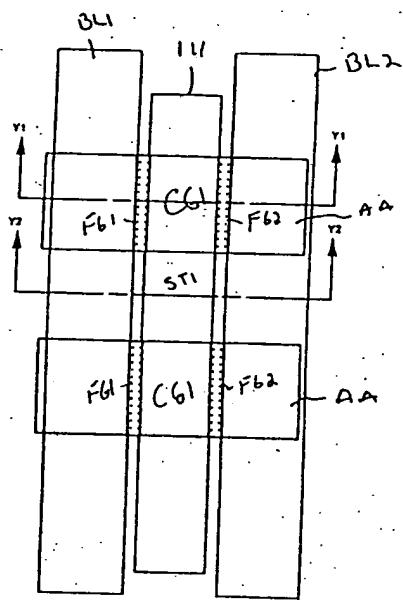


FIG 10A

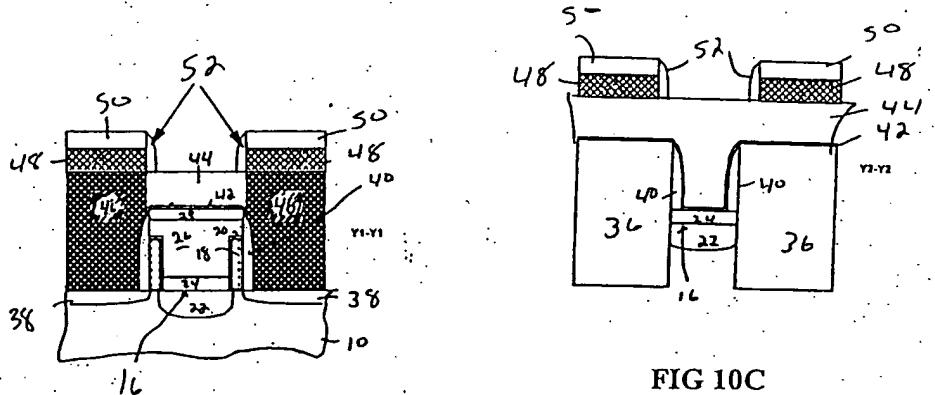


FIG 10C

FIG 10B

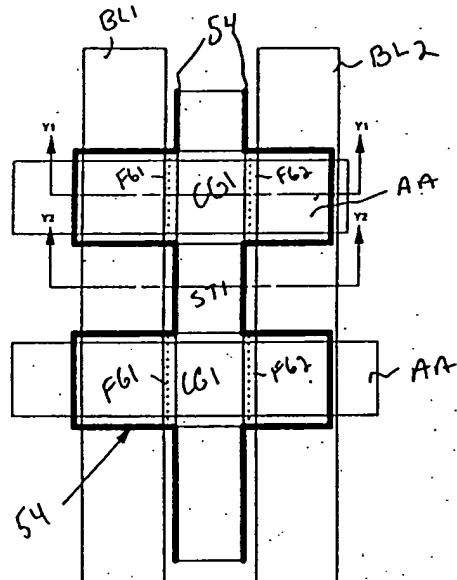


FIG 11A

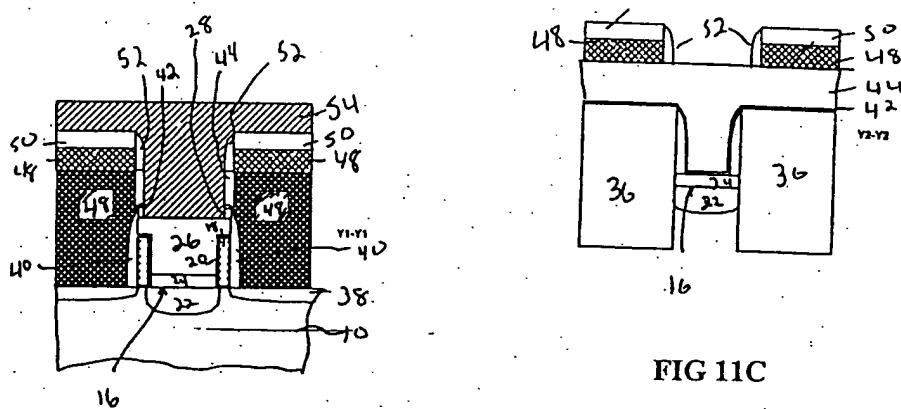


FIG 11B

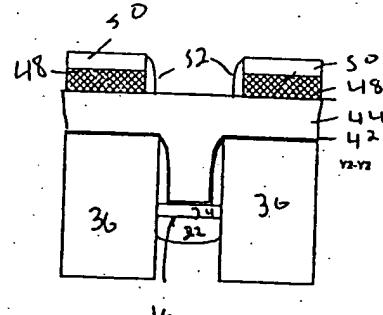


FIG 11C

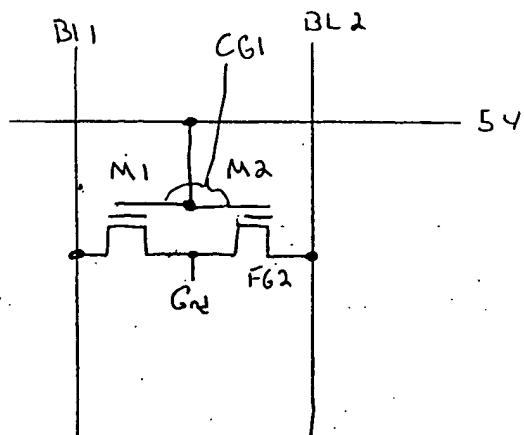


FIG 12